

L Number	Hits	Search Text	DB	Time stamp
2	0	(rambus.as. and @ad<19991229) and "module 5"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/07/11 08:25
1	298	rambus.as. and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/07/11 08:50
3	118	rambus.as. and @ad<19991229 and dram	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/07/11 09:00
4	372	(rambus near6 dram) and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/07/11 09:15
5	388	(output adj driver\$1) and memory and "second transistor" and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/07/11 09:26
6	164	(memory near4 blocks) and "activated simultaneously" and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/07/11 09:27
7	10	(memory near4 blocks) near5 "activated simultaneously" and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/07/11 10:16
8	15495	(clock near4 signal) and (control\$3 adj (module unit)) and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/07/11 10:18
9	6045	(clock near4 signal) and (control\$3 adj (module unit)) and (multiplex\$2 mux) and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/07/11 10:19
10	142	(clock adj enable adj signal) and (control\$3 adj (module unit)) and (multiplex\$2 mux) and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/07/11 10:25
11	0	(clock adj enable adj signal) and ((control\$3 adj (module unit)) near4 (comprises consists) near4 (multiplex\$2 mux)) and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/07/11 10:26
12	9	((control\$3 adj (module unit)) near4 (comprises consists) near4 (multiplex\$2 mux)) and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/07/11 12:36
13	7193	((data near line) bus) same characteristics) and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/07/11 12:45
16	839	temperature and "slew rate" and adjust\$4 and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/07/11 13:06

17	125	"second transistor" near "control signal" and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/11 14:02
18	0	(control\$3 same "column signal" same "read signal" and memory) and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/11 14:04
19	9758	(control\$3 same column same read and memory) and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/11 15:00
20	5536	(multiplex\$2 mux) and ((select control) near clock) and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/11 15:10
21	4584	((multiplex\$2 mux) same respon\$2 same clock) and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/11 15:14
-	1712	driver\$1 same ((coupl\$3 connect\$3) near "10" (bus pci interconnect)) same control\$3 same transistor\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 08:13
-	1388	(driver\$1 same ((coupl\$3 connect\$3) near "10" (bus pci interconnect)) same control\$3 same transistor\$1) and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/03 15:51
-	659	((driver\$1 same ((coupl\$3 connect\$3) near "10" (bus pci interconnect)) same control\$3 same transistor\$1) and @ad<19991229) and (degrad\$5 noise)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/03 15:59
-	0	((driver\$1 same ((coupl\$3 connect\$3) near "10" (bus pci interconnect)) same control\$3 same transistor\$1) and @ad<19991229) and (degrad\$5 noise)) and capacitane	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/03 15:59
-	544	((driver\$1 same ((coupl\$3 connect\$3) near "10" (bus pci interconnect)) same control\$3 same transistor\$1) and @ad<19991229) and (degrad\$5 noise)) and capacit\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/03 16:13
-	367	((driver\$1 same ((coupl\$3 connect\$3) near "10" (bus pci interconnect)) same control\$3 same transistor\$1) and @ad<19991229) and (degrad\$5 noise)) and capacit\$5) and (rambus memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/03 16:21
-	582	"output driver" same (memory ram\$3 \$3ram) same bus	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/03 16:25
-	445	("output driver" same (memory ram\$3 \$3ram) same bus) and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/03 16:55
-	192	("output driver" same (memory ram\$3 \$3ram) same bus) and @ad<19991229) and transistor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/03 17:22

-	14	yim-sung-min.in. yim-sungmin.in. han-kyu-han.in. han-kyuhan.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 17:18
-	0	(transist\$3 same bus same degrad\$5) and @ad<12291999	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/08 08:45
-	139	transist\$3 same bus same degrad\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/08 08:44
-	108	(transist\$3 same bus same degrad\$5) and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/08 08:45
-	9	(yim-sung-min.in. yim-sungmin.in. han-kyu-han.in. han-kyuhan.in.) and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 17:11
-	1112	rambus and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/08 10:17
-	172	rambus and @ad<19991229 and degrad\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/08 10:20
-	152	rambus and @ad<19991229 and degrad\$5 and bus	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/08 10:26
-	1412	"output driver" and "memory device"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/08 10:26
-	951	"output driver" and "memory device" and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/08 10:27
-	668	"output driver" and "memory device" and @ad<19991229 and transist\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/08 10:27
-	211	"output driver" and "memory device" and @ad<19991229 and transist\$3 and "reference voltage"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/08 10:31
-	50	"output driver" and "memory device" and @ad<19991229 and transist\$3 and "reference voltage" and degrad\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/08 12:18
-	0	SCSI and diconnect and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/08 12:20

-	163	capacitance and bus and @ad<19991229 and degrad\$5 and "output driver" and transist\$2 and memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/08 14:43
-	9	"column cycle signal"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/08 14:44
-	0	710/305.clas.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/08 16:37
-	859	710/305.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/08 16:37
-	560	710/305.ccls. and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/08 16:49
-	47	710/118.ccls. and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/08 16:53
-	275	326/90.ccls. and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/08 17:23
-	29	"semiconductor memory devices" and "memory cells" and "output drivers" and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/08 17:24
-	3	"memory module" and "semiconductor memory device" and bus and "output drivers" and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/09 09:42
-	62071	RAM and bus and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/09 09:43
-	1382	"output driver" and RAM and bus and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/09 10:06
-	0	"deactivated output driver" and RAM and bus and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/09 10:06
-	0	"disabled output driver" and RAM and bus and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/09 10:06
-	1	"disabled output driver" and RAM and bus and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/09 13:31

-	16971	memory and drivers and bus and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/09 13:32
-	30544	memory and driver and bus and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/09 13:32
-	2616	memory and "output driver" and bus and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/09 13:36
-	1489	memory and "output driver" and bus and @ad<19991229 and (deactivated disabled "turned off")	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/09 14:37
-	2	5319755.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 16:19
-	343	gillingham.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/09 14:46
-	14	gillingham-peter.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/09 15:08
-	6129	(memory adj (block cell unit)) and driver and (controller processor arbitrator) and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/09 16:22
-	633	(memory adj (block cell unit)) and "output driver" and (controller processor arbitrator) and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/09 16:30
-	926	(memory adj (block cell unit device)) and "output driver" and (controller processor arbitrator) and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/09 16:31
-	485	(memory adj (block cell unit device)) and "output driver" and (controller processor arbitrator) and @ad<19991229 and semiconductor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/09 16:32
-	5767	365/189.01,230.06.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 08:16
-	4860	365/189.01,230.06.ccls. and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 11:06
-	2570	365/189.01.ccls. and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 11:07

-	2391	365/230.06.cccls. and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 11:08
-	47	710/118.cccls. and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 11:16
-	275	326/90.cccls. and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 12:56
-	1157	327/108.cccls. and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 12:56
-	61	327/594.cccls. and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 12:57
-	400	327/437.cccls. and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 13:04
-	278	327/112.cccls. and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 13:56
-	38810	(multiplexer multiplexor mux) and control and clock and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 13:57
-	1394	(multiplexer multiplexor mux) and control and "clock enable" and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 14:00
-	222	((multiplexer multiplexor mux) same control same "clock enable") and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 15:00
-	17553	711.clas.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 15:01
-	17553	711.clas. and @py<2004	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 15:04
-	12657	711.clas. and @py<2002	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 15:06
-	9203	("tri state" tristate "tri-state") and bus and @ay<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 15:14

-	3	("tri state" tristate "tri-state")and bus and @ay<19991229 and device adj (disconnect uncoupling)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 15:14
-	131	("tri state" tristate "tri-state")and bus and @ay<19991229 and device near4 (disconnect\$2 uncoupl\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 15:21
-	0	((disconnect\$2 uncouple\$2 remove\$1) adj "from bus") and @ay<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 15:22
-	0	((disconnect\$2 uncouple\$2 remove\$1) near4 from near4 bus) and @ay<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 15:24
-	6176	((disconnect\$2 uncouple\$2 remove\$1) near4 bus) and @ay<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 15:28
-	1407	((disconnecting) near7 bus) and @ay<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 15:35
-	7	simione.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 15:35
-	84	simeone.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 15:36
-	0	simeone-john.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 15:36
-	2	simeone-john-b.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 15:36
-	2	5297260.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 16:19
-	57	"calibration information" and "control signal" and transist\$2 and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 16:54
-	18452	"data line" and (characteristics features) and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 16:55
-	265	"data input/output line" and (characteristics features) and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 16:56

-	1	("data input/output line" near5 (characteristics features)) and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 16:58
-	174	((data near line) near5 (characteristics features)) and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 17:01
-	0	"read control signal" and "calibration information" and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 17:01
-	325	"read control" and calibration and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/10 17:02
-	3	"column cycle" and "read control" and (multiplex\$2 mux) and @ad<19991229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/11 08:25